# RAJESH MADHIRA

# **Law Student**

#### **Contact**

## **Address**

A-118, VSH, IIT-Kharagpur Kharagpur, India 721302

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#### E-mail

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#### **Skills**

Legal Research Technical

## **Interests**

Law and technology

About me

Education

15 years of experience in core networking technologies and building high performance teams. Making a career transition into the legal domain. Seeking an internship position to understand the legal dynamics of the copyright industry.

2022-08 - Current

#### Rajeev Gandhi School of IP Law - IIT Kharagpur

Pursuing an LLB degree with honours in Intellectual Property Law.

- CGPA 7.28
- Member of Pro Bono club

2002-07 - 2006-06

## SASTRA University - Thanjavur

B. Tech in Electronics and Communication Engineering.

CGPA 7.67

## Summary

Technical design, Implementation and maintenance of complex and time critical embedded software.

Specialise in ASIC data path programming for Routing and timing protocols across CISCO catalyst (3k/9k) switches and IOT routers.

Architect of the phase and frequency synchronisation infra on CISCO IOT rugged series router IR8300.

Implemented various RFC's, IEEE specs and ITU-T standards and customised them as per customer requirements across various hardware and Operating System platforms.

RFC3376(IGMPv3) /IEEE1588 (PTP) /IEEE802.1AS /IEEEC37.238 /ITU-T G.8275.1 / ITU-T G.8265.1

2014-10 - 2022-08

Senior Technical Leader @ Cisco Systems India Private Limited, Bengaluru

2013-01 - 2014-10

Software Engineer @ Aruba (HP Enterprise), Bengaluru

2008-09 - 2013-01

Software Engineer @ Cisco Systems India Private Limited., Bengaluru

2006-11 - 2008-02

Software Engineer @ Subex Ltd., Bengaluru

**Work Summary** 

Work History